## II. AMENDMENTS TO THE SPECIFICATION

Applicant requests that the following amendments be entered with respect to the specification:

At page [0026]:

[0026] With reference to the accompanying drawings, FIG. 2 is a block diagram of a piecewise constant (PWC) based simulation system 10 in accordance with the invention. Simulation system 10 includes a memory 12, a processing unit (PU) 14, input/output devices (I/O) 16 and a bus 18. A database 20 may also be provided for storage of data relative to processing tasks. Memory 12 includes a program product 22 that, when executed by PU 14, comprises various functional capabilities described in further detail below. Memory 12 (and database 20) may comprise any known type of data storage system and/or transmission media, including magnetic media, optical media, random access memory (RAM), read only memory (ROM), a data object, etc. Moreover, memory 12 (and database 20) may reside at a single physical location comprising one or more types of data storage, or be distributed across a plurality of physical systems. PU 14 may likewise comprise a single processing unit, or a plurality of processing units distributed across one or more locations. I/O 16 may comprise any known type of input/output device including a network system, modem, keyboard, mouse, scanner, voice recognition system, CRT, printer, disc drives, etc. Additional components, such as cache memory, communication systems, system software, etc., may also be incorporated into simulation system 10.

Serial No. 10/709,949